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In re Applicant:

David A. Kiss

Serial No.: 10/017,031

Filed: October 30, 2001

For: Packaged Combination Memory
for Electronic Devices

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Art Unit: 2815

Examiner: Sheila Clark

Atty Docket: ITL.0696US
P13281

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Commissioner for Patents
Washington, DC 20231

REPLY TO PAPER NO. 2

Sir:

In response to the office action mailed July 3, 2002, reconsideration is requested in view of the following remarks.

REMARKS

Claim 1 calls for what essentially amounts to a processor-based system in a single package. Claim 1 calls for a packaged integrated circuit device including a processor, a volatile memory, and a cross point memory.

Such a packaged device could effectively provide complete solutions for processor-based system applications. For example, the volatile memory can temporarily store data that is permanently stored in a cross point memory. In some cases no mechanical memory, such as a hard disk drive, need be provided. Thus, the combination of the three elements inside one package provides an advantageous solution.

The single packaged product could provide essentially any memory need that any processor-based system might have. Namely, the combination of a volatile memory with a cross point memory provides an effective memory system for a processor-based system. See e.g., the

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specification at page 5, line 24 though page 6, line 5. Particularly, in the case of portable processor-based systems, the system set forth in claim 1 would be particularly advantageous.

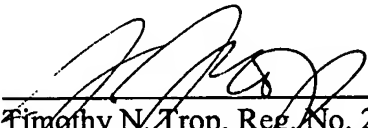
None of the cited references teach the combination within a package of a processor and the two different types of memory. No where is a single packaged part capable of implementing the complete solution set forth in claim 1. Mauritz certainly teaches away from putting all the components in one package and, rather, teaches a conventional non-integrated, non-packaged, computer system. All that Haba suggests is a packaged integrated circuit with a processor and memory die. There is nothing in Haba that suggests the complete solution claimed.

Failing the teaching anywhere in the prior art of the complete solution claimed or a rationale to modify the references to teach that solution, the claimed invention is non-obvious. Merely the fact that someone teaches a variety of memory devices to be packaged together with a processor does not teach the specific combination within one package of a processor, a volatile memory, and a cross point memory. For the same reasons, claim 10 patentably distinguishes over the art of record. Similarly, claim 18 patentably distinguishes over the art of record.

In view of these remarks, the application is now in condition for allowance and the Examiner's prompt action in accordance therewith is respectfully requested.

Respectfully submitted,

Date: July 18, 2002



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